

**Amendments to the Specification:**

On page 1, prior to the first paragraph which begins on line 2, please insert the following:

FIELD OF THE INVENTION

On page 1, prior to the second paragraph which begins on line 4, please insert the following:

BACKGROUND OF THE INVENTION

On page 3, prior to the paragraph which begins on line 22, please insert the following:

SUMMARY OF THE INVENTION

Please replace the paragraph which begins on page 3, lines 29-30, with the following rewritten paragraph:

This object is achieved by a variable field device for process automation[[,]] ~~as defined in claim 1.~~ including: a sensor module SM for measured value detection; a signed processing module SPM connected to the sensor module SM; a processor module PM, connected to a communication module CU for connection of the field device with a superordinated control-evaluation unit, wherein the signal processing unit SPM and the processor module PM are provided in the form of a reprogrammable logic device LD.

On page 4, prior to the paragraph which begins on line 11, please insert the following:

BRIEF DESCRIPTION OF THE DRAWINGS

Please replace the paragraph which appears on page 5, lines 10 - 25, with the following replacement paragraph:

Fig. 2 shows a field device in the form of a typical sensor S1. Sensor S1 includes a measurement transducer MT, which is connected with a sensor unit SU. The sensor unit SU is followed by a digital signal processor DSP. The digital signal processor is connected with a system processor MP. The system processor MP is connected via a communications unit CU with the data bus DB. Furthermore, the system processor MP is connected with an analog unit AU, which has a plurality of inputs, outputs I/O. Serving for display of the measurement value and for manual input is a display and operating unit DO, which is likewise connected with the system processor MP. The power supply of the sensor S1 is cared for by a power supply unit PS, which is connected to the various hardware components of the sensor S1, as indicated by the dashed lines. Power supply can occur externally or over the data bus DB.

National Phase of PCT/EP03/05130

Please replace the paragraph which appears on page 4, lines 11-19, with the following revised paragraph:

Fig. 1 is a data ~~Data~~ bus system in schematic presentation;

Fig. 2 is a schematic presentation of a conventional field device having various hardware components;

Fig. 3 is a schematic presentation of a field device of the invention;

Fig. 4 is a schematic presentation of a reprogrammable logic device with flash memory;  
and

Fig. 5 is a schematic presentation of a logic device associated with memory and loading controller.

On page 4, prior to the paragraph which begins on line 19, please insert the following:

DESCRIPTION OF THE PREFERRED EMBODIMENTS